# PATMOS2016

## 26th International Workshop on Power and Timing Modeling, Optimization and Simulation

## Bremen, GERMANY
## September 21 to 23, 2016



PATMOS has a history of 26 years and it is one of the first conferences world-wide to focus on low power.. The traditional scope of the PATMOS conference series has mainly been about and around the design of circuits and architectures optimized for highest performance at lowest power consumption. But meanwhile, power-efficiency has become extremely important for many more areas spreading far beyond this traditional R&D niche. Energy-efficient ICT (Information and Communication Technology) infrastructures are a key issue of local and global economies. Some predict that until the year 2030, if current trends continue, the electricity consumption caused by the Internet to grow by up to 30 times. Energy prices will grow substantially. The next generation of oil and gas seismic simulations, for instance, will require orders of magnitude more computational power. Already during the past 11 years the price of crude oil increased by a factor of 9 with significantly increasing tendency in the future. The strong increase of wireless communication and the growth of cloud computing will further contribute to this trend. A future peta- or exa-flop supercomputer would need its own power plant if the gap between computation and power consumption could not be resolved: It is the intention of PATMOS 2016 to think beyond current solutions such that the very wide gap between computation and the massive energy consumption for ICT infrastructures can be closed. PATMOS 2016 will be held in Bremen, Germany, collocated with VARI 2016.

### Topics of Interest:

Authors are invited to submit manuscripts of original unpublished research. The topics of interest include, but are not limited to:

- Timing and Performance
- Low Power and Thermal-aware Design
- Compilers, operating systems and runtime systems
- FPGAs and GPU-based accelerators
- Power-efficient High-performance ICT and Data Centers
- Application-specific power efficiency by algorithmic and analytic efforts
- Case studies

Accepted and presented papers will be published in IEEE Xplore®. All manuscripts will be blind reviewed by at least three members of the program committee. Submissions should be a complete manuscript of novel unpublished work (not to exceed 8 pages of single spaced text, including figures and tables) or, in special cases, may be a summary of relevant work.
(more details at the conference webpage)


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### Important Dates:

Submission deadline:
May 8, 2016

Notification of acceptance:
June 26, 2016

Camera-ready papers due:
July 24, 2016

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